

GE DOCKET:141601 & 144981
INVENTOR: VAFI ET AL.
RS DOCKET: GE.0005
RS ATTORNEY: ELLIS B. RAMIREZ

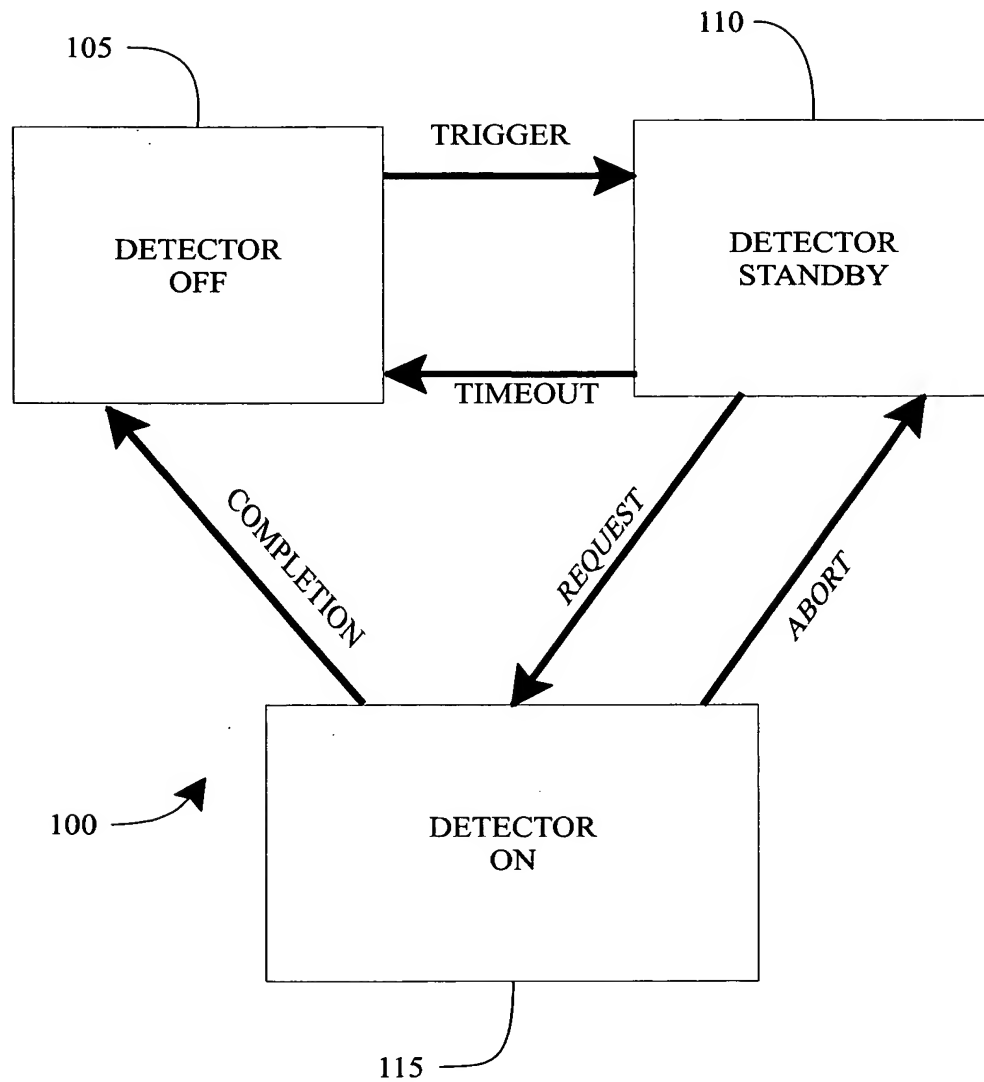


FIG. 1

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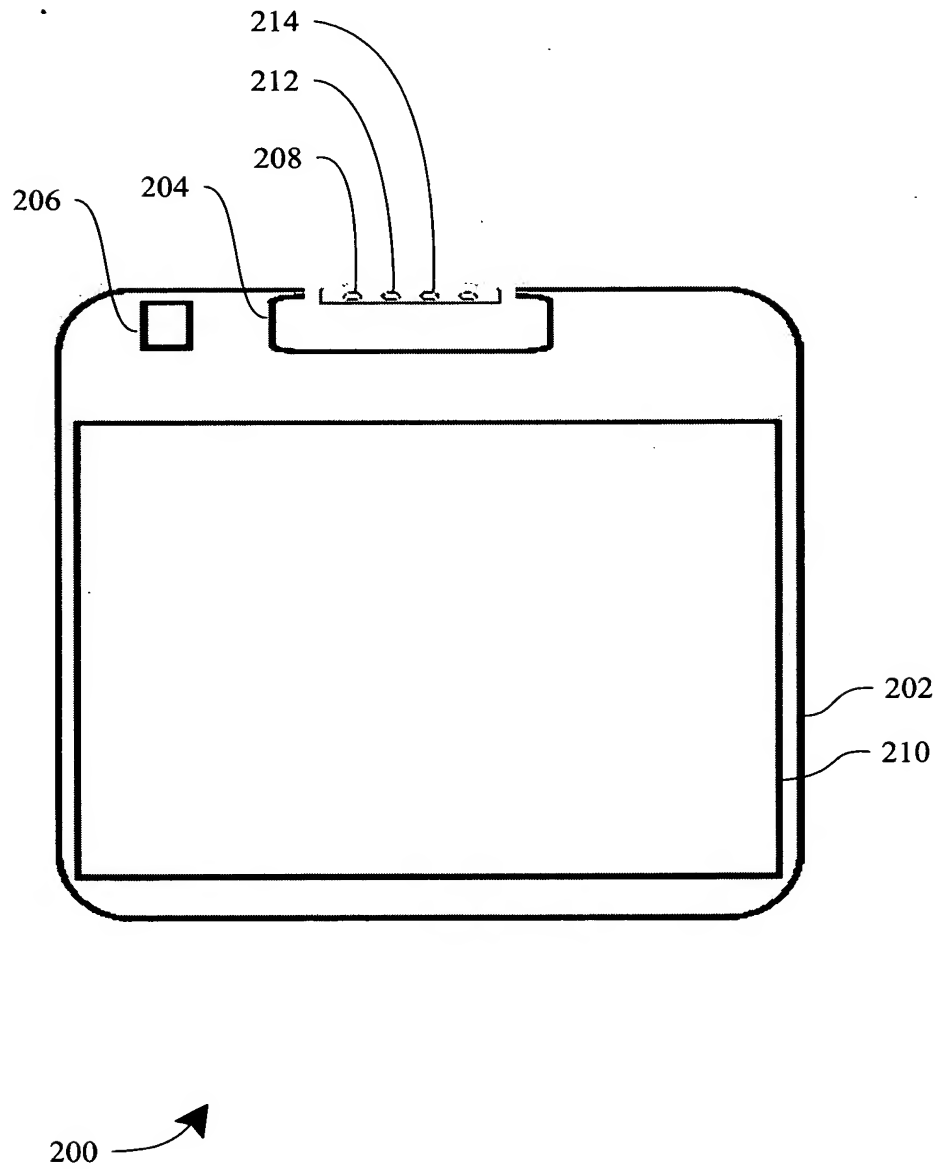


FIG. 2

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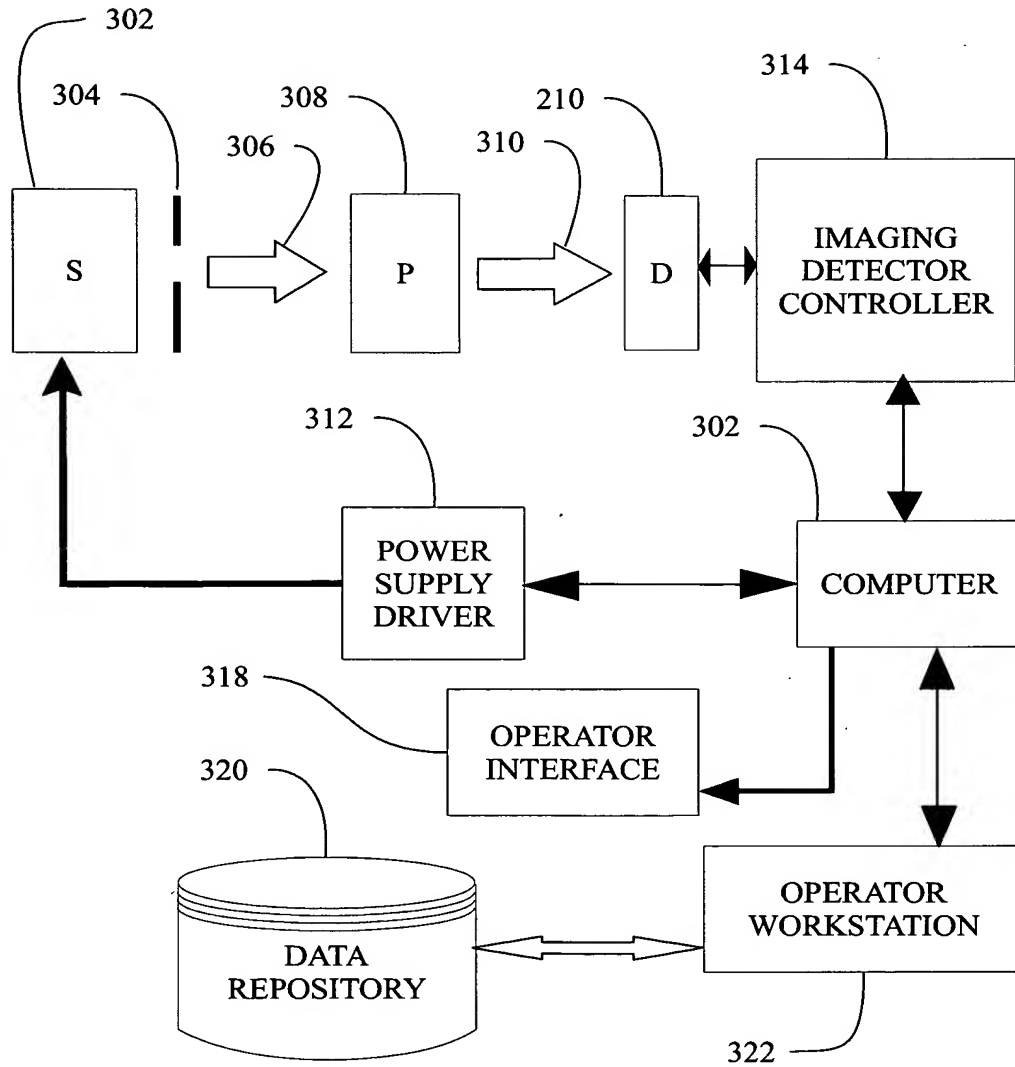
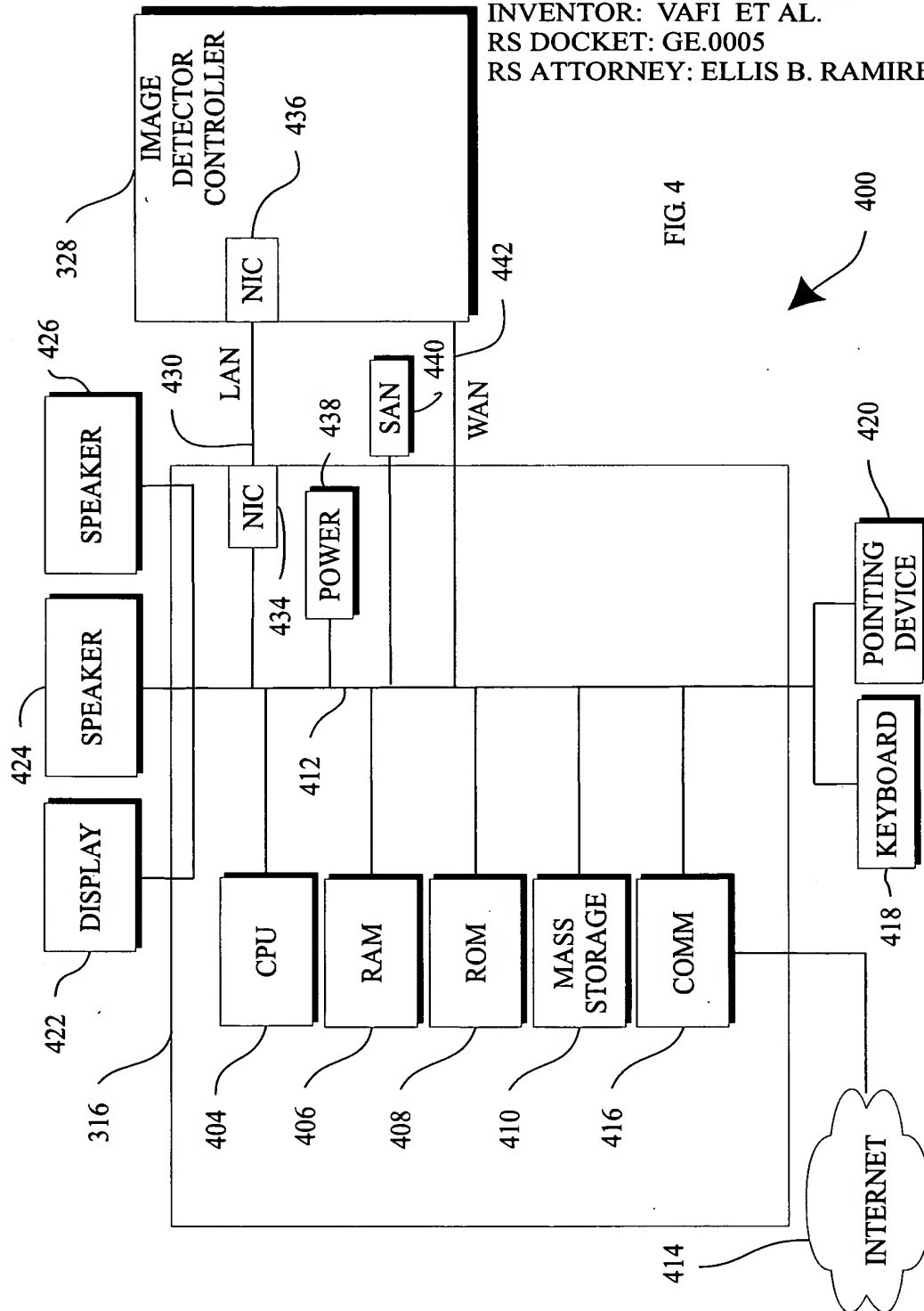


FIG. 3

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The diagram illustrates a detector system 210. At the top, a row of control blocks includes an INDICATOR 206, ACTIVATION 208, READER 212, RESET 214, and POWER 504. These are connected to a central DETECTOR CONTROLLER. To the left, an IDC (Inter-Digit Circuit) 314 is connected to the controller. Below the controller, a REFERENCE REGULATOR CIRCUITRY 40 and an ENVIRONMENTAL CONDITION DEVICE 506 are connected. The main component is a detector array 42, which is a grid of resistors (R) and capacitors (C). A vertical bus 44 connects the capacitors to the reference regulator circuitry. A horizontal bus 45 connects the resistors to the environment. A detailed view 50 shows a resistor 54 on a grid with a transistor 70.

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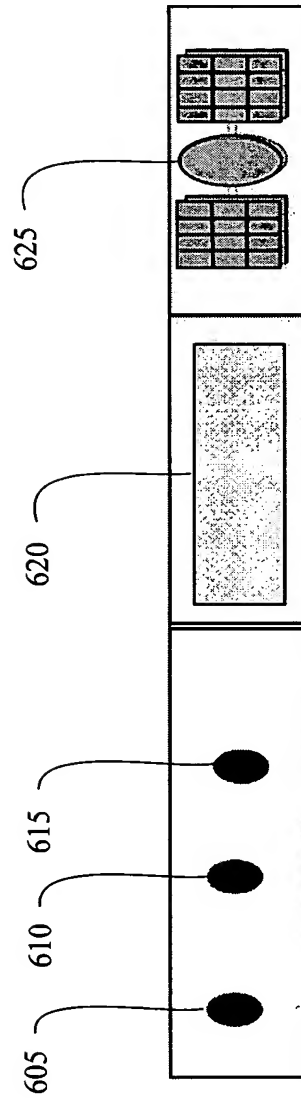
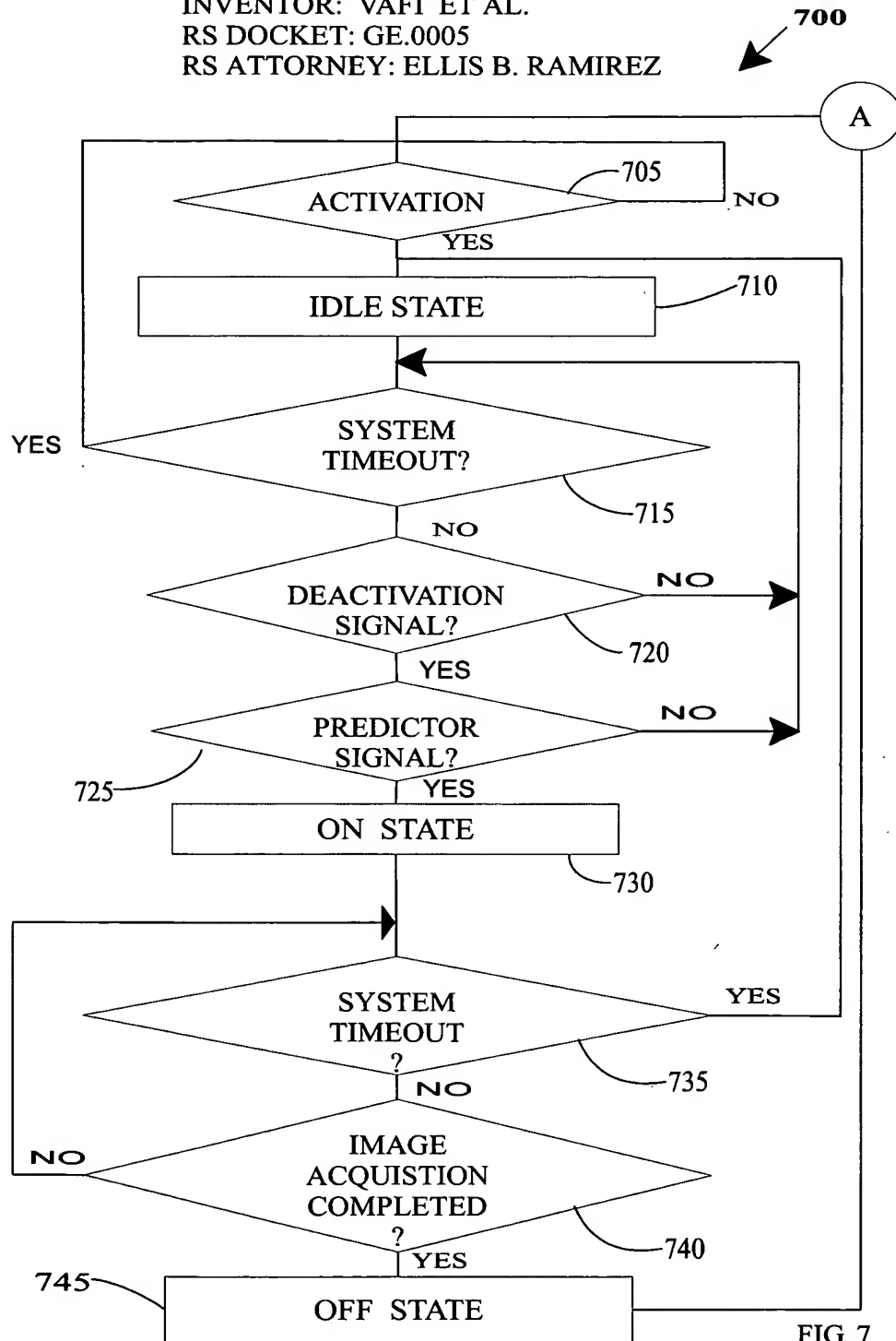


FIG. 6

206 →



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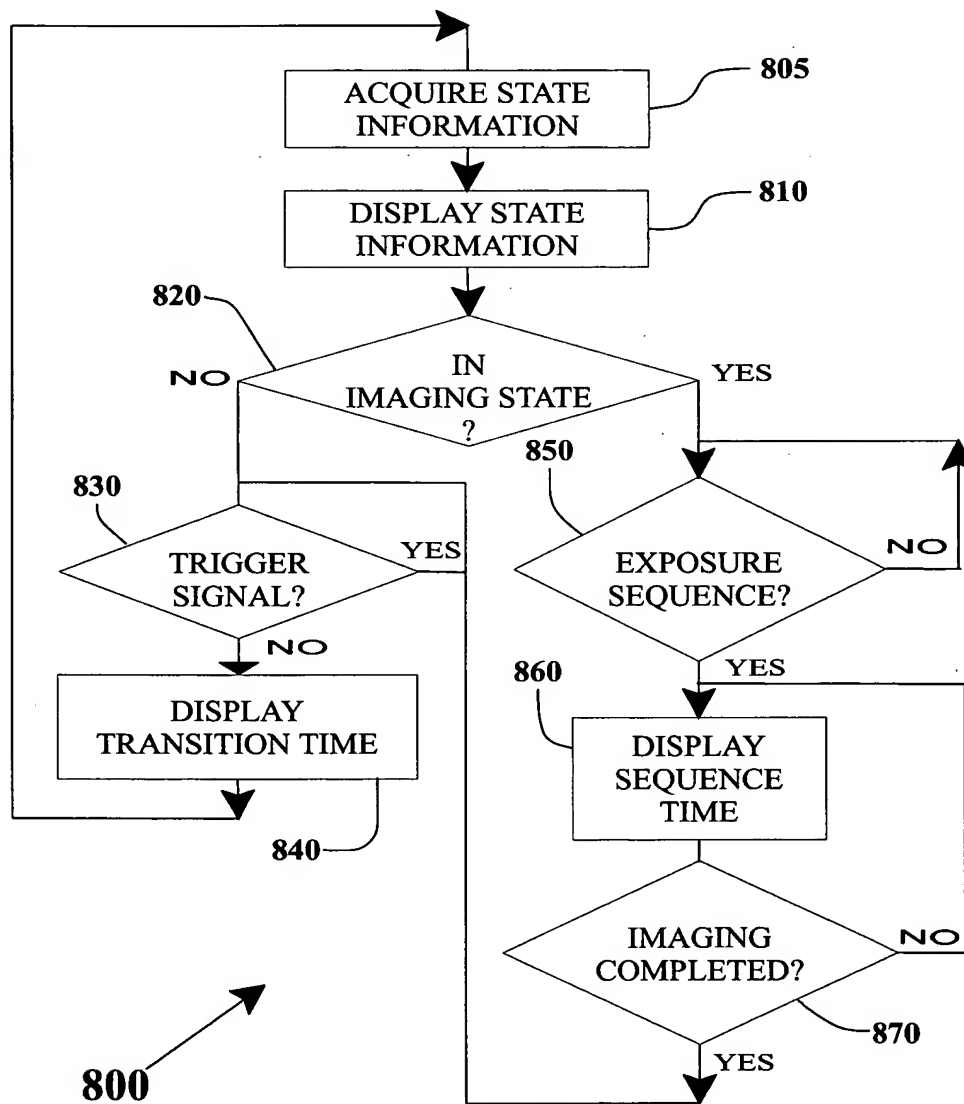
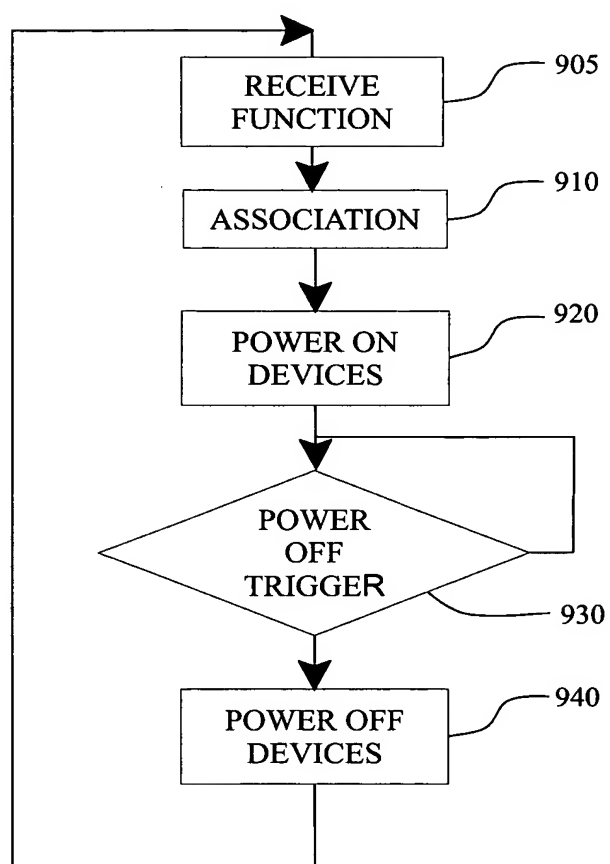


FIG. 8

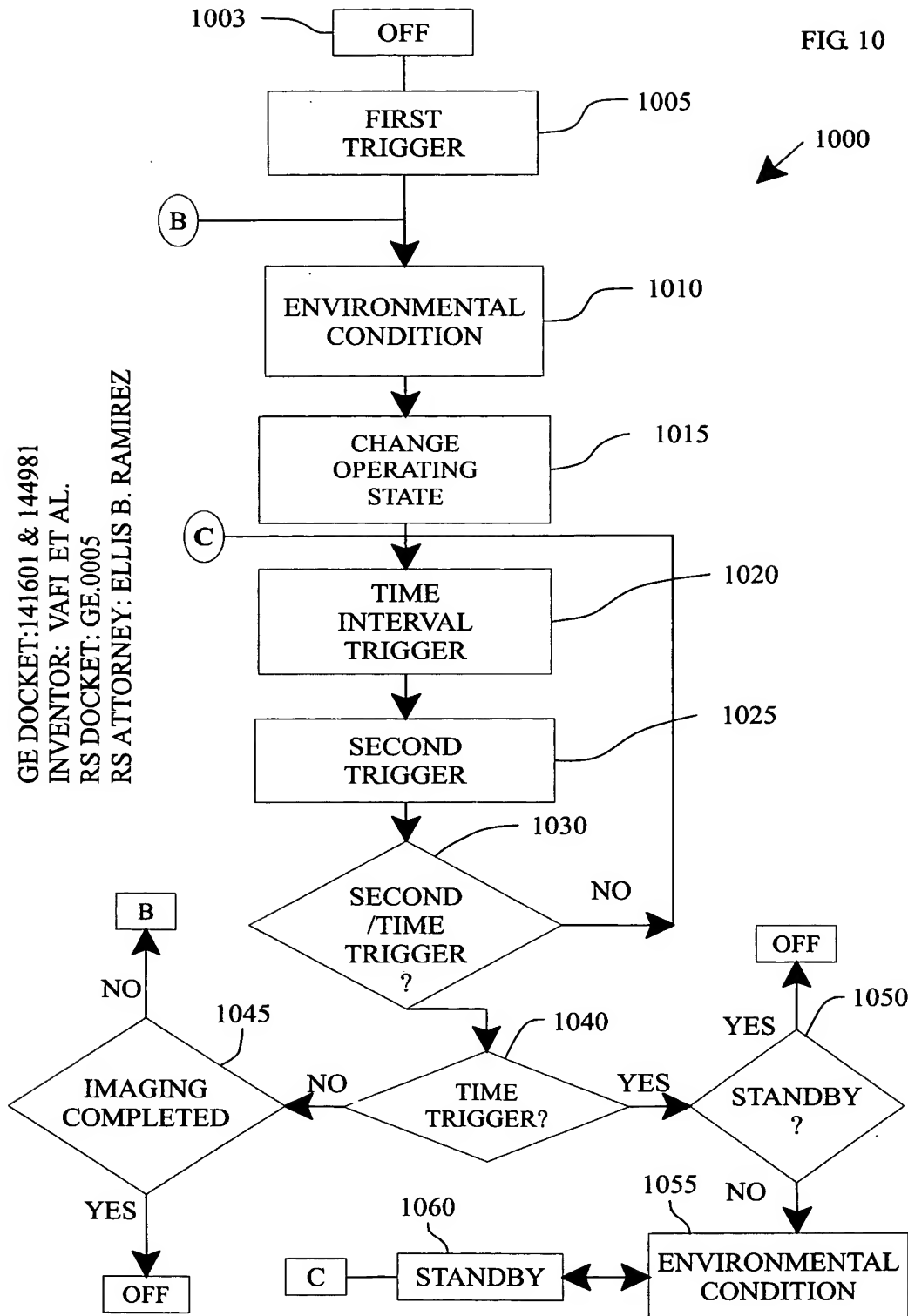
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900

FIG. 9

FIG 10



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